

FIG. 1
PRIOR ART

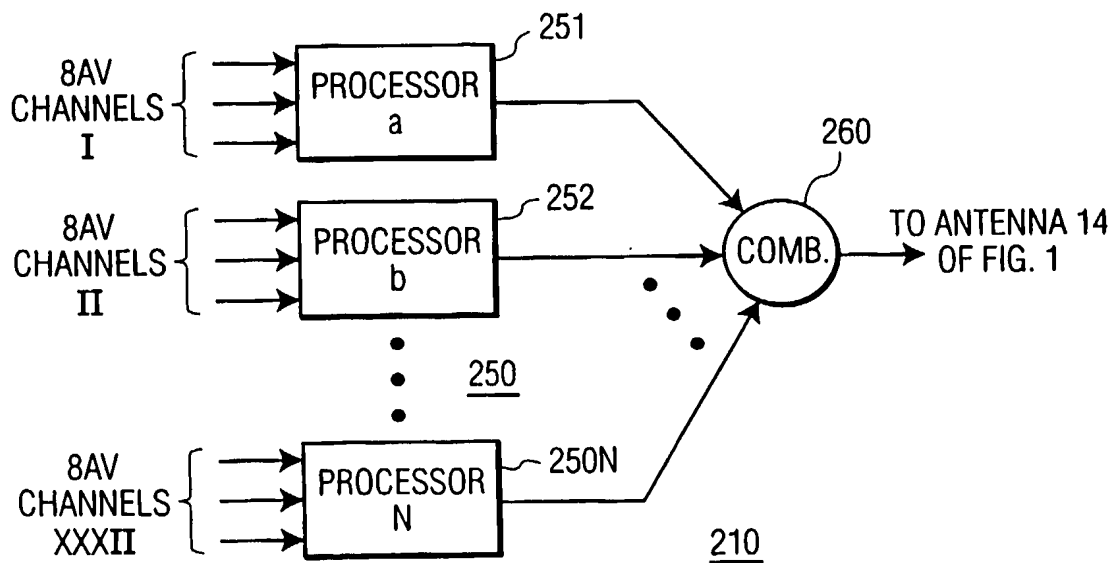


FIG. 2a
PRIOR ART

2/7

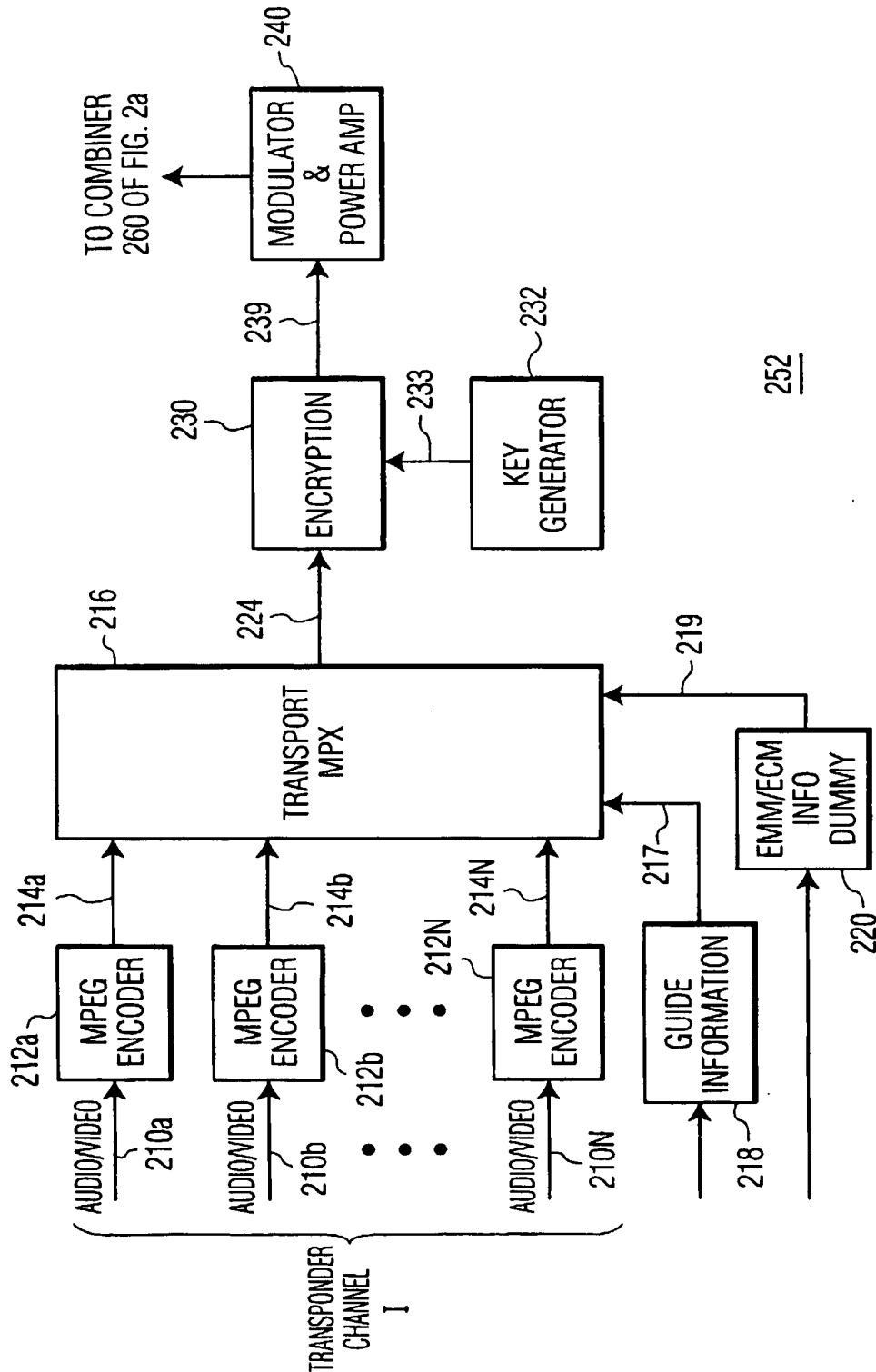


FIG. 2b
PRIOR ART

3/7

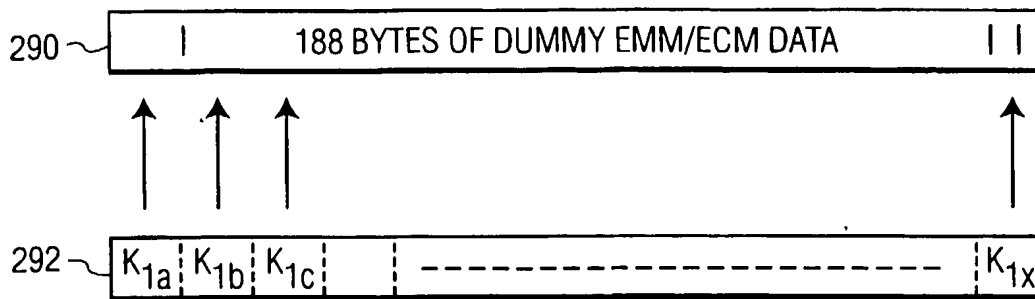


FIG. 2c
PRIOR ART

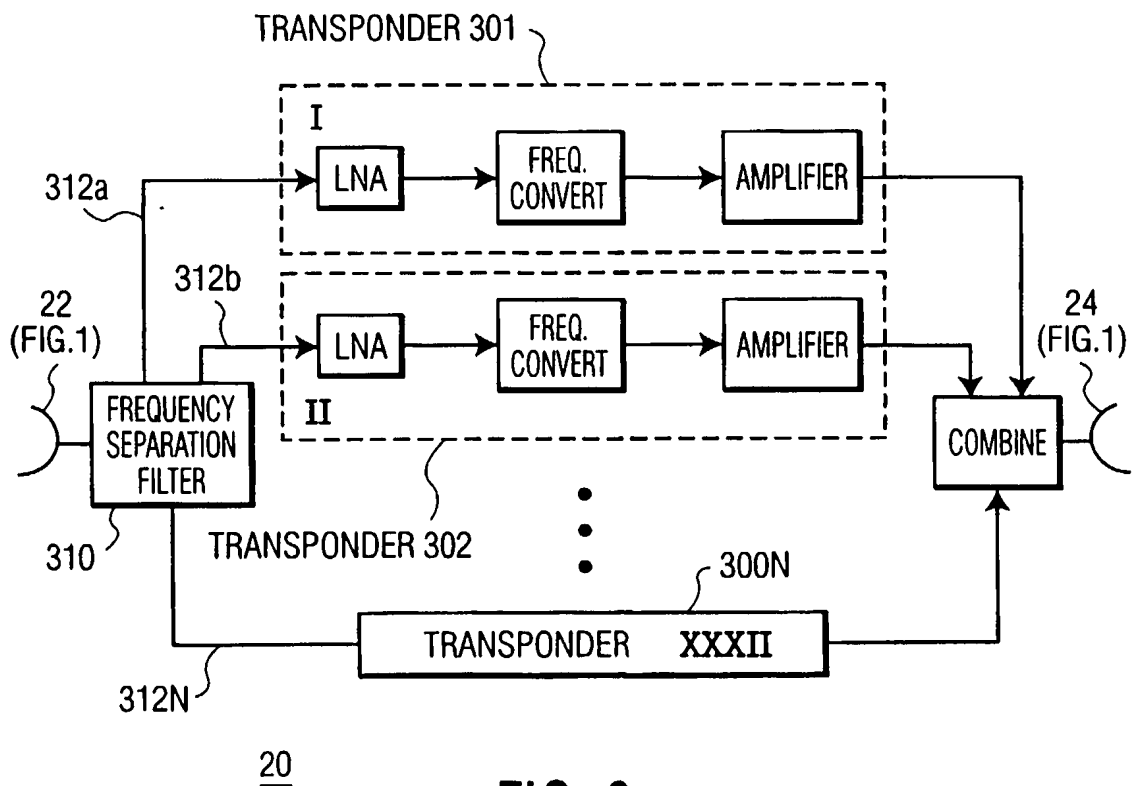


FIG. 3
PRIOR ART

4/7

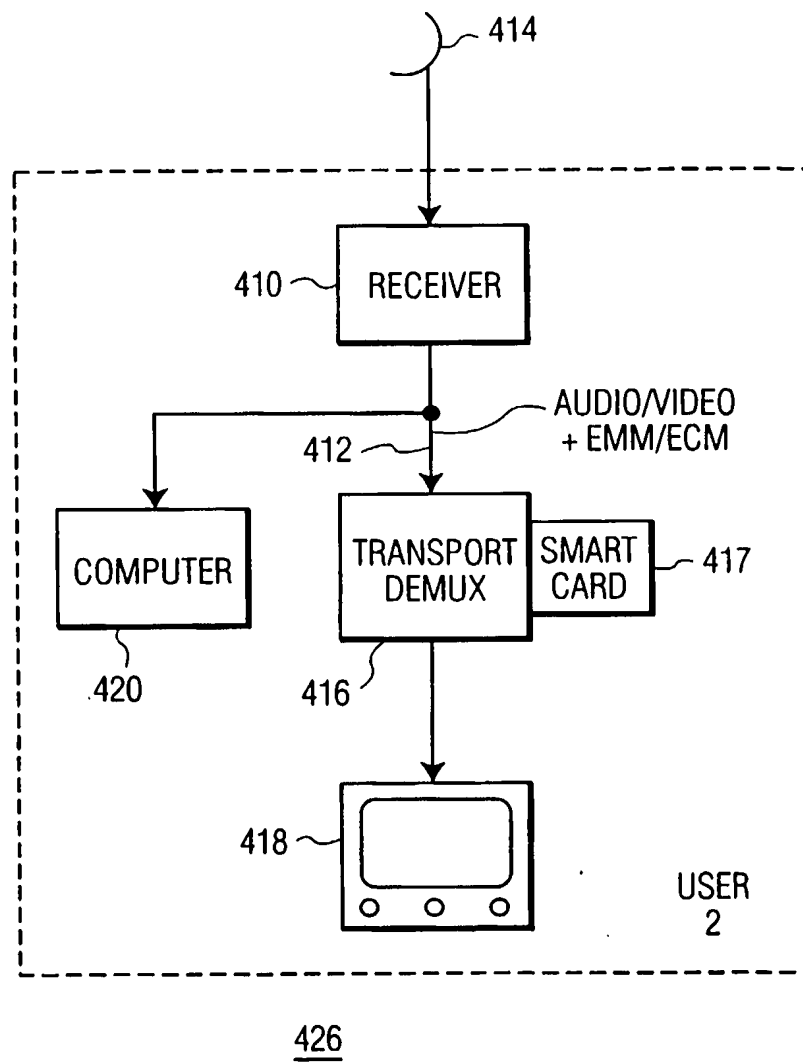


FIG. 4

5/7

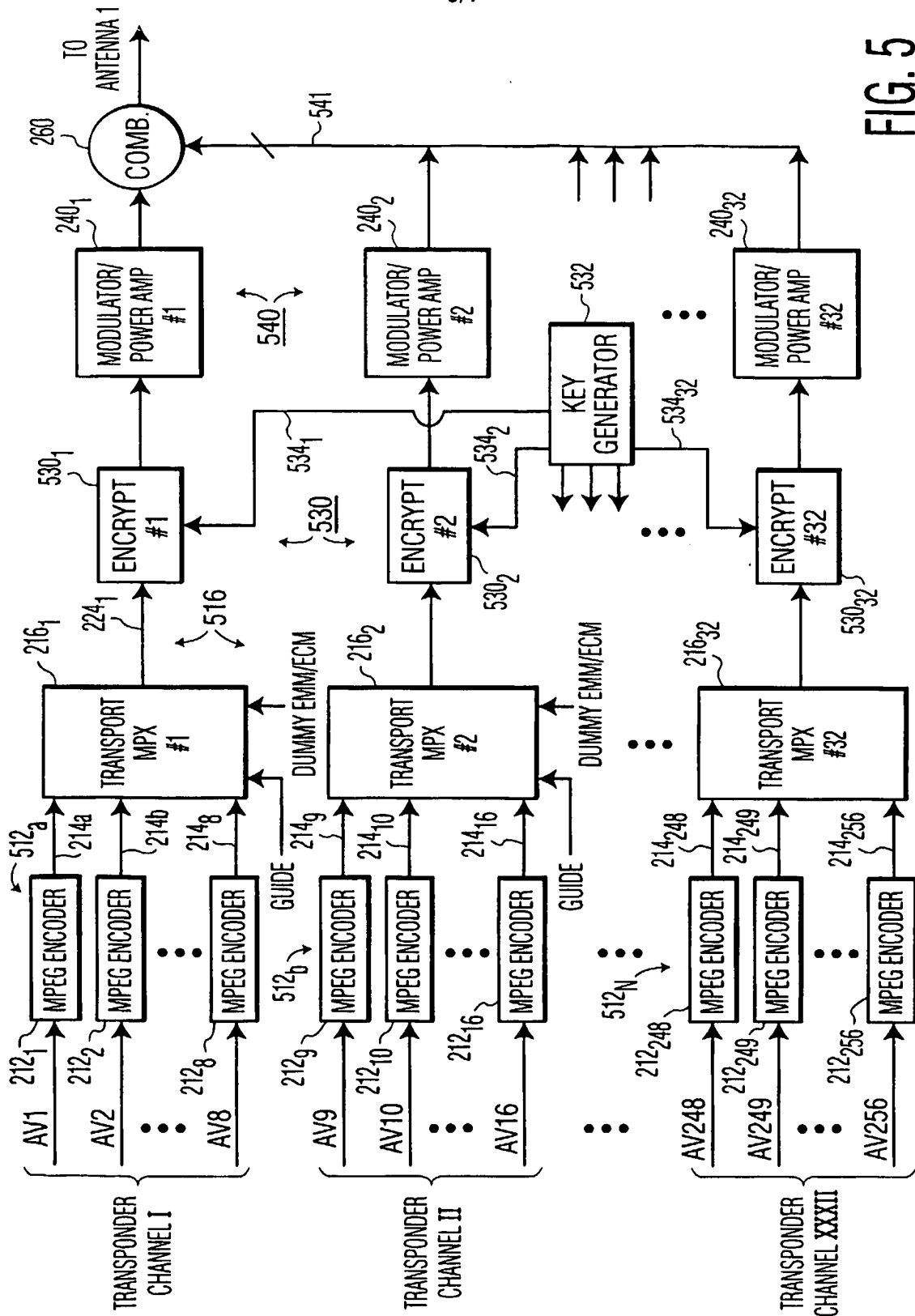


FIG. 5

6/7



FIG. 6

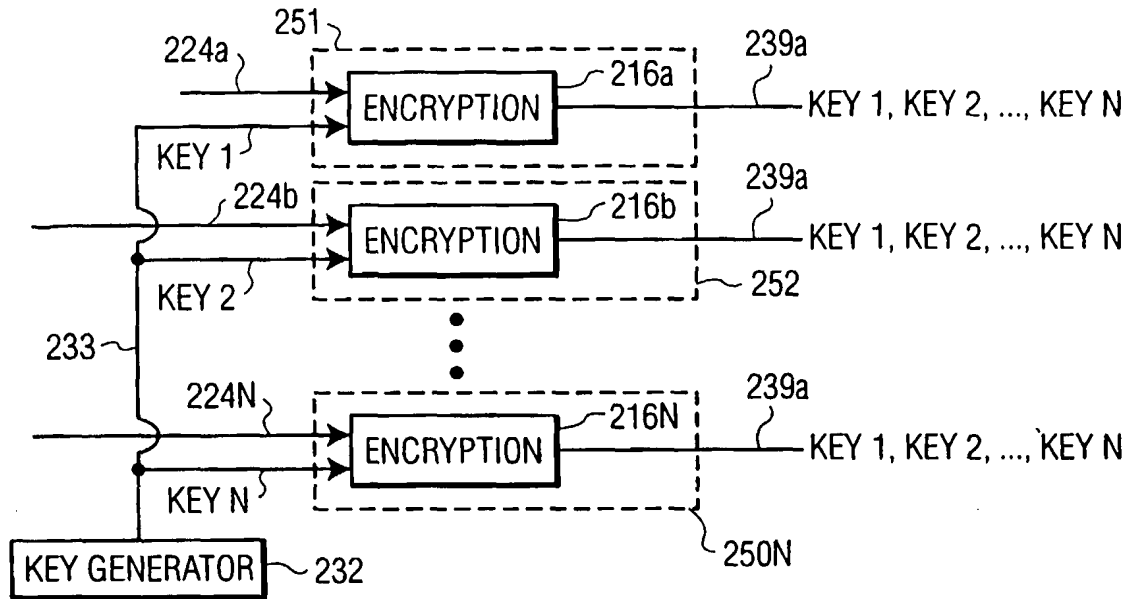


FIG. 7a

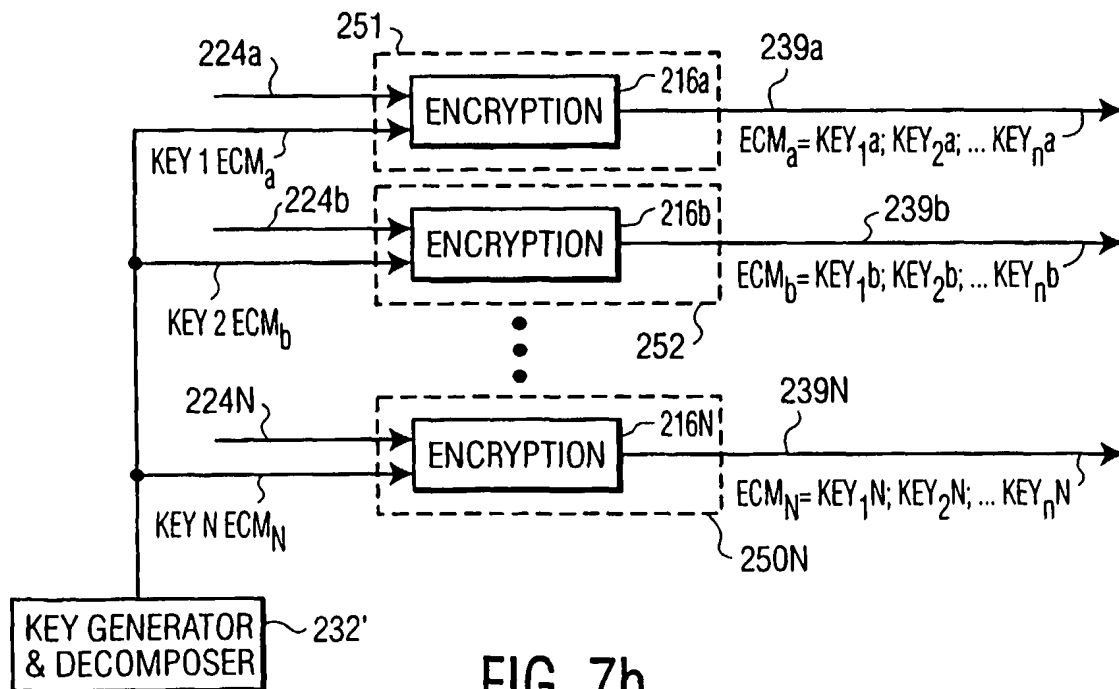


FIG. 7b

7/7

TRANSPONDER 1	V_1	V_1	A_1	E_1	V_1	V_1	A_1	E_1	V_1	V_1	A_1	E_1	...
TRANSPONDER 2	V_2	V_2	A_2	E_2	V_2	V_2	A_2	E_2	V_2	V_2	A_2	E_2	...
⋮													
TRANSPONDER n	V_n	V_n	A_n	E_n	V_n	V_n	A_n	E_n	V_n	V_n	A_n	E_n	...

FIG. 8a

TRANSPONDER 1	V_1	V_1	A_1	E_1	V_1	V_1	A_1	E_2	V_1	V_1	A_1	E_n	...
TRANSPONDER 2	V_2	V_2	A_2	E_2	V_2	V_2	A_2	E_n	V_2	V_2	A_2	E_1	...
⋮													
TRANSPONDER n	V_n	V_n	A_n	E_n	V_n	V_n	A_n	E_1	V_n	V_n	A_n	E_2	...

FIG. 8b